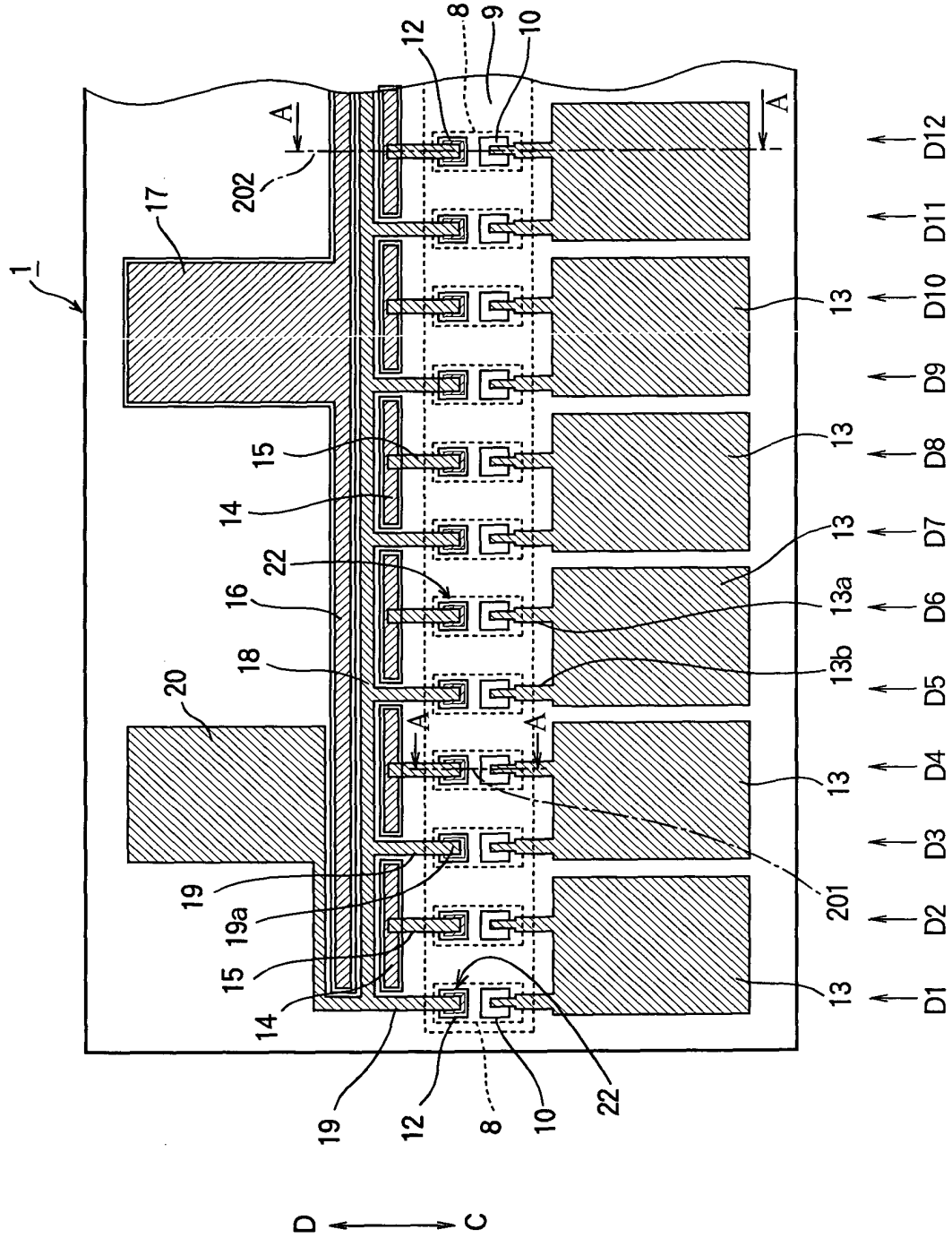


FIG. 1



A cross-sectional view of a semiconductor device 8. The device features a substrate 2 with a base layer 11. A central region contains a stack of layers 3, 4, 5, and 6, which are collectively indicated by bracket 7. This central stack is flanked by two side regions, each containing a stack of layers 3, 4, and 5. The top surface of the device is covered by a layer 10, which is patterned into a central rectangular region 11 and two side regions 12. A layer 13a is located beneath the central region 11. A layer 15 is located beneath the side regions 12. A layer 22 is located beneath the central region 11. A layer 8 is located beneath the side regions 12.

Figure 1 is a schematic diagram of a semiconductor device 1. The device consists of a substrate 21, which is shown as a hatched area. A series of square elements 10 are arranged in a row on the substrate. A curved line 21a is drawn over the elements. A vertical dashed line A-A is shown on the right, with a curved arrow 203 indicating a rotation or bending of the substrate around it.

A cross-sectional view of a container assembly. The container body (2) is shown with a lid (7) on top. A seal (10) is located between the lid and the container body. The seal is composed of a first layer (21) and a second layer (21a). The first layer (21) is a thin, flat layer, and the second layer (21a) is a thicker, more complex layer that forms the main sealing surface.

FIG.4A

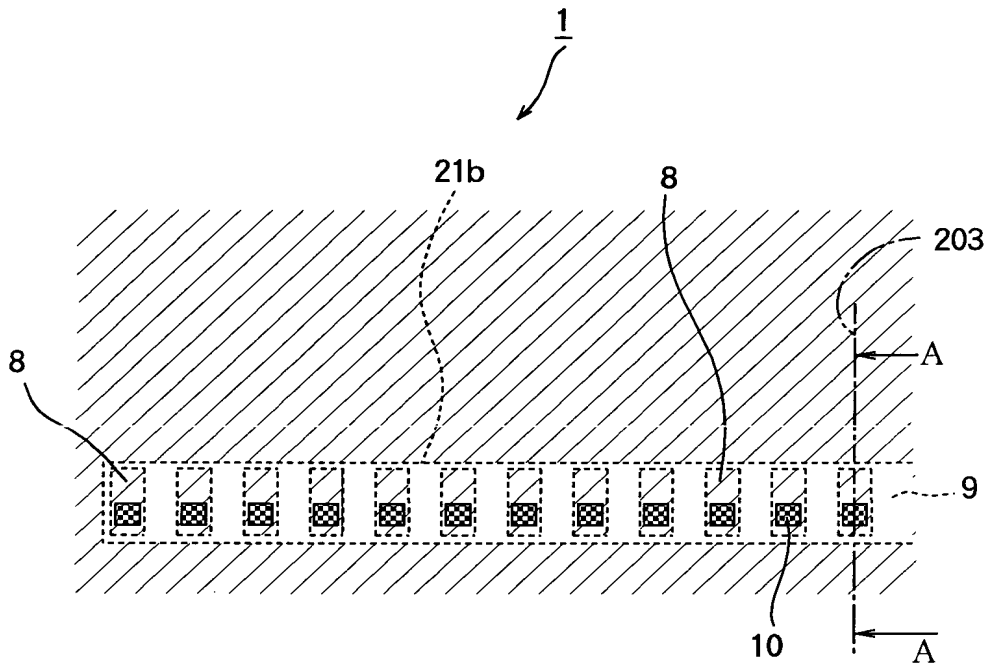


FIG.4B

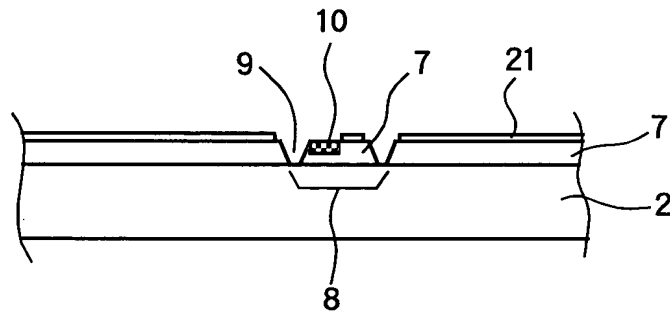


FIG. 5A

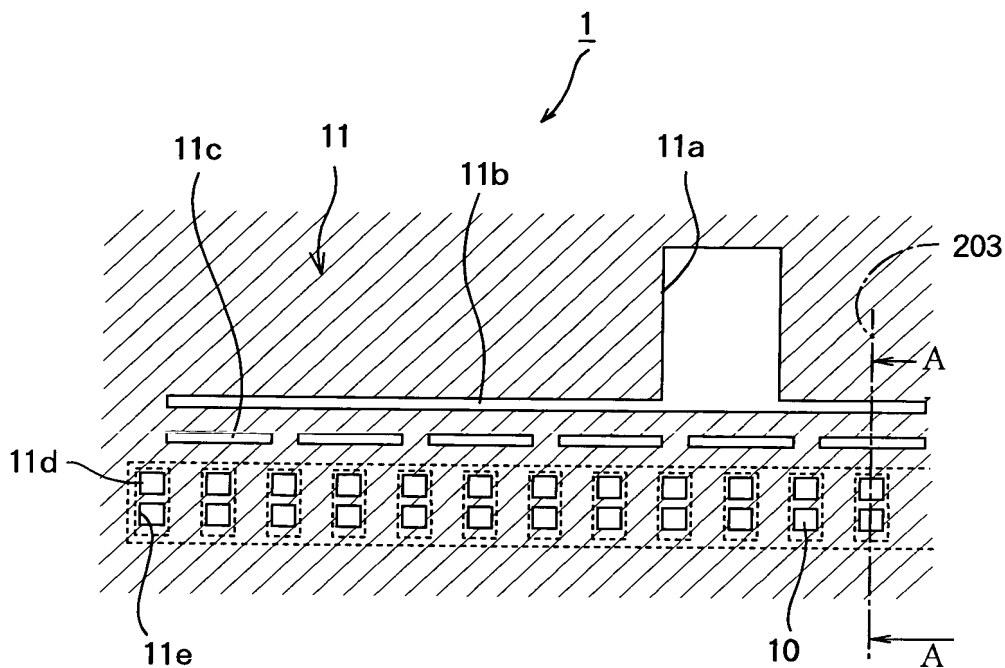


FIG. 5B

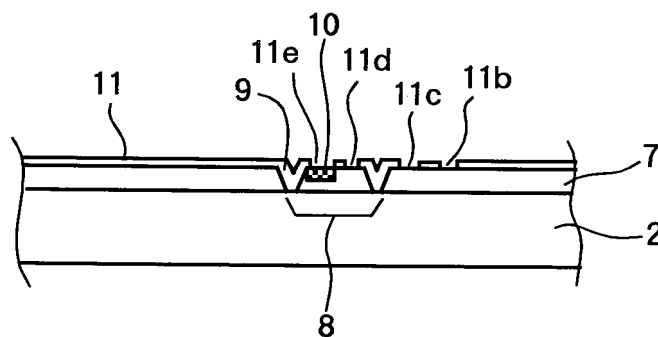


FIG. 6A

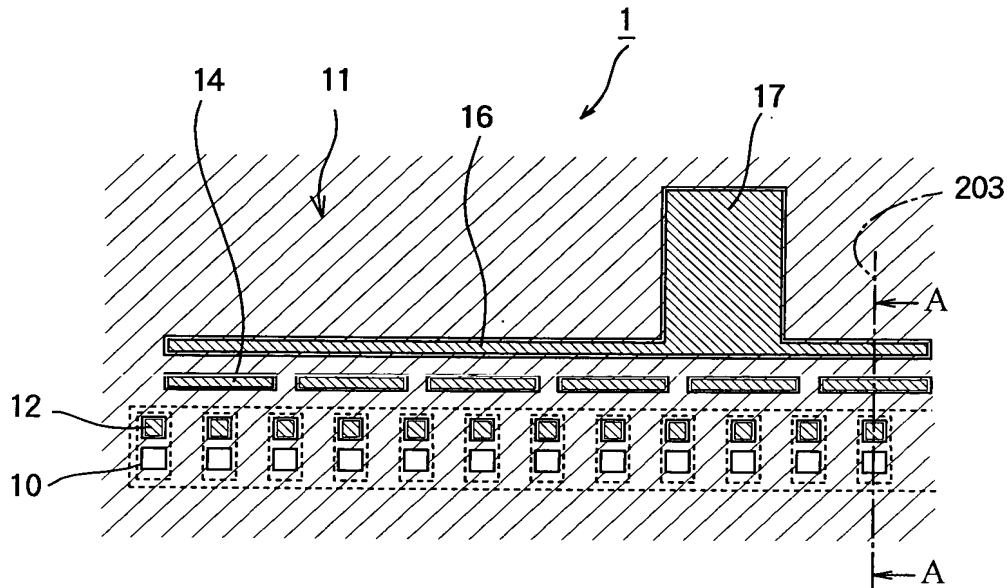


FIG. 6B

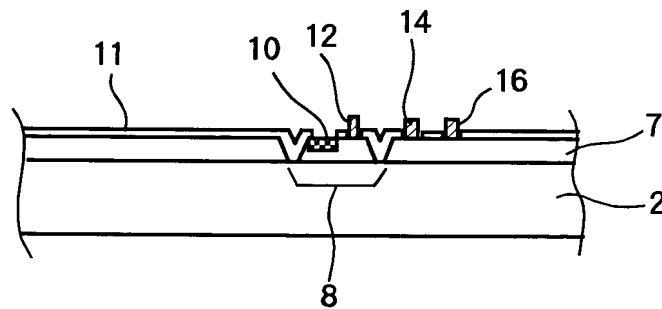


FIG. 7A

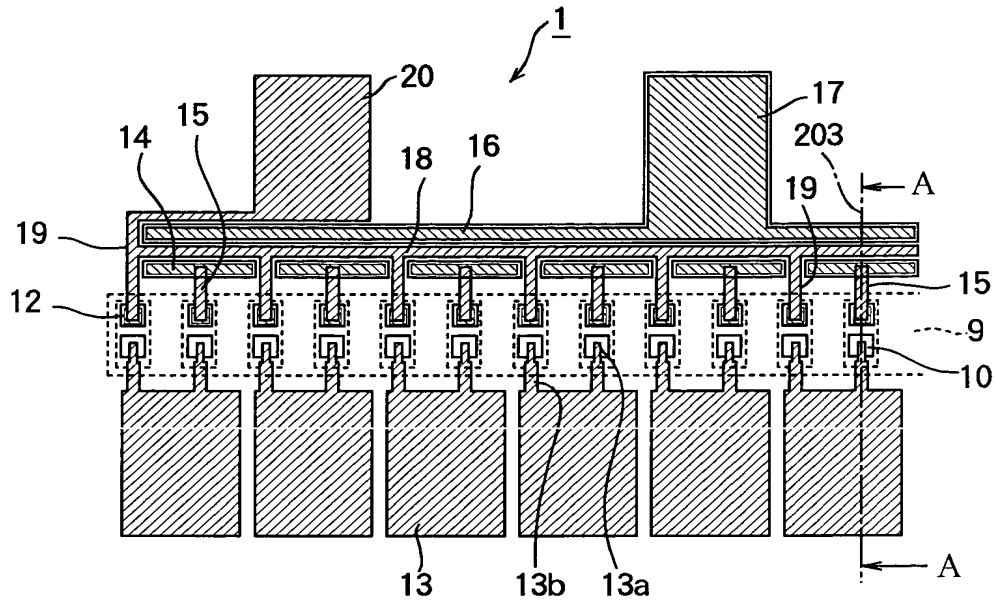


FIG. 7B

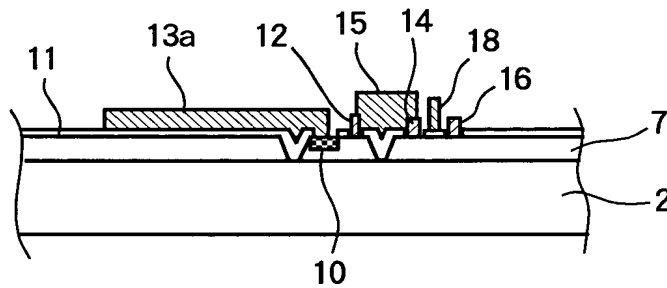


FIG. 8

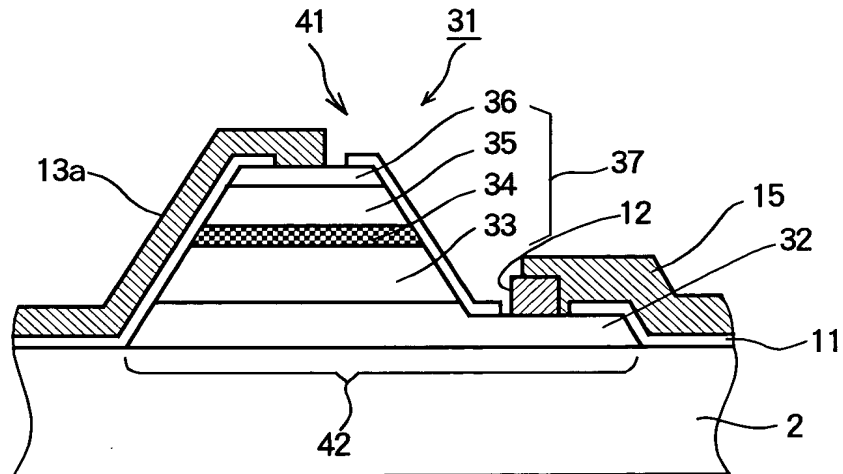


FIG. 9A

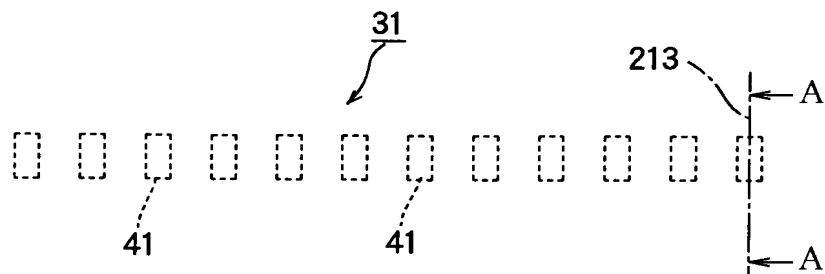


FIG. 9B

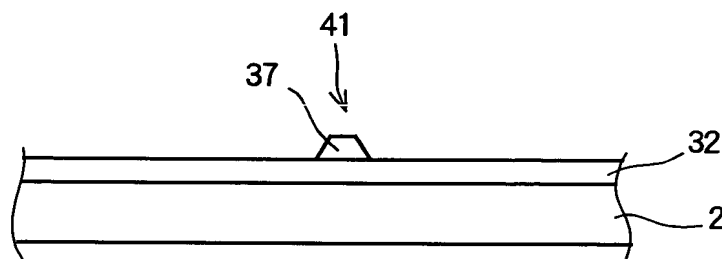


FIG. 10A

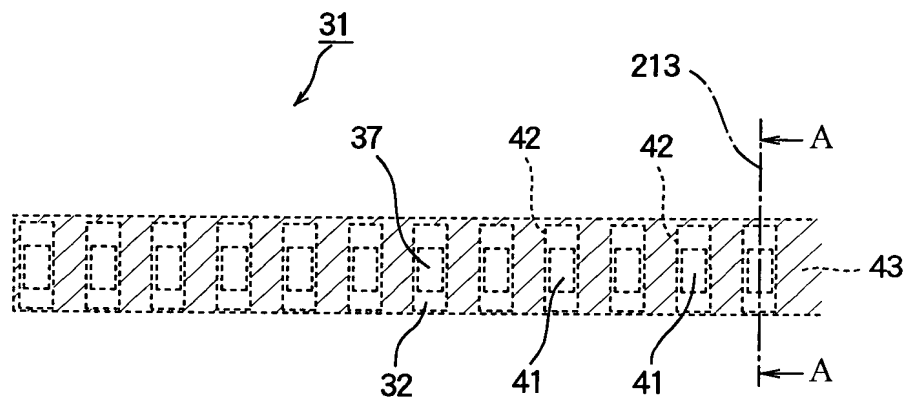
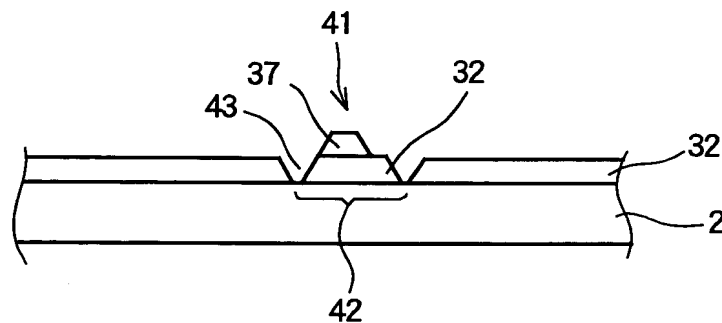


FIG. 10B



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FIG. 11A

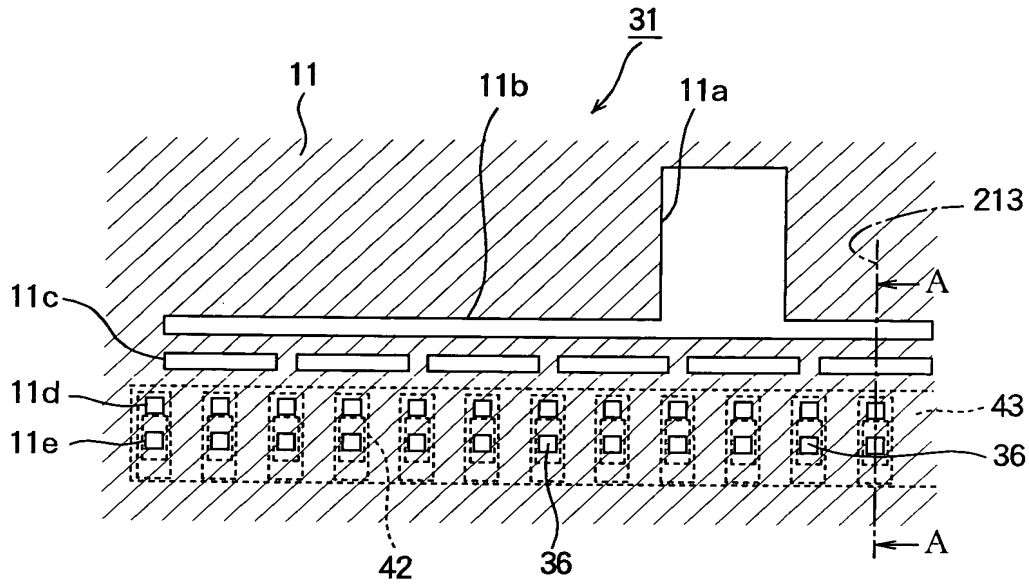
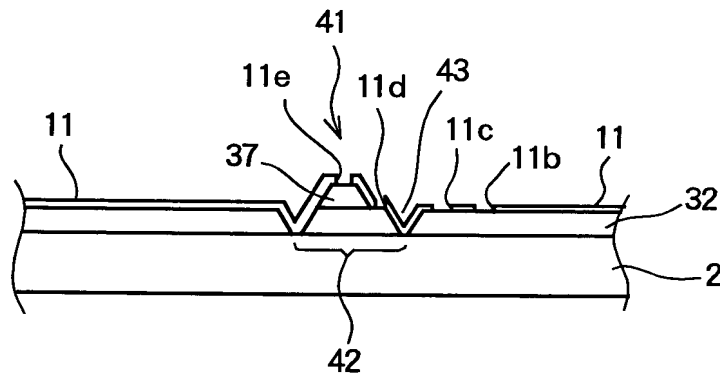


FIG. 11B



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FIG. 12A

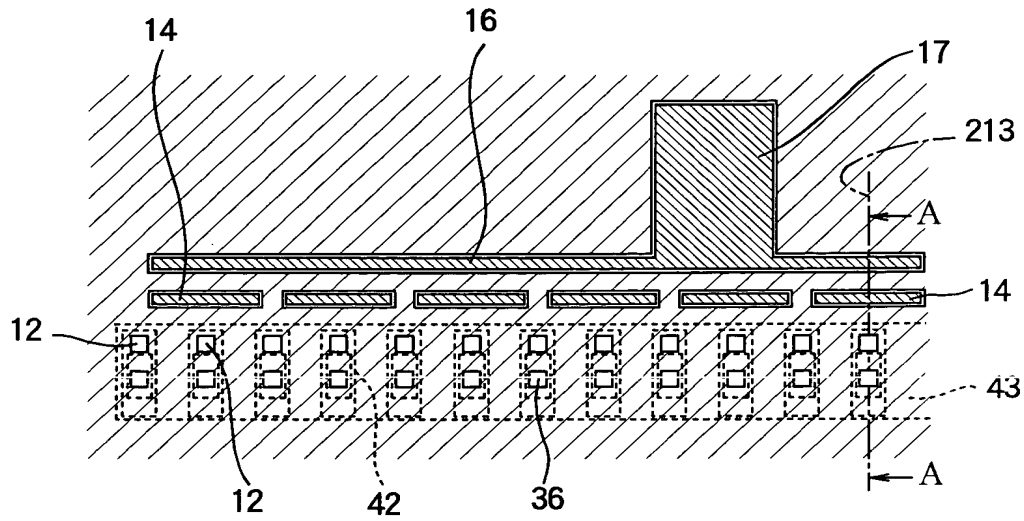
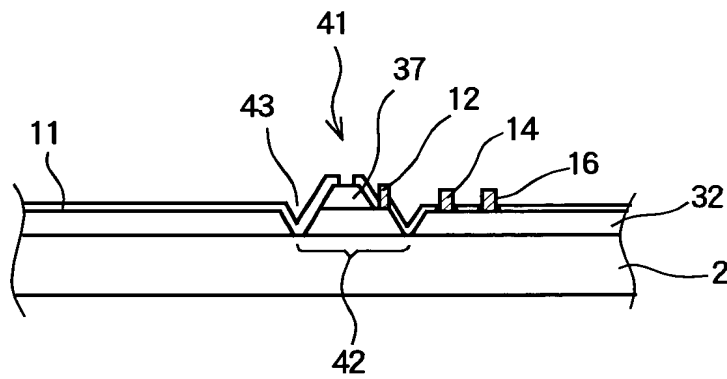


FIG. 12B



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FIG.13A

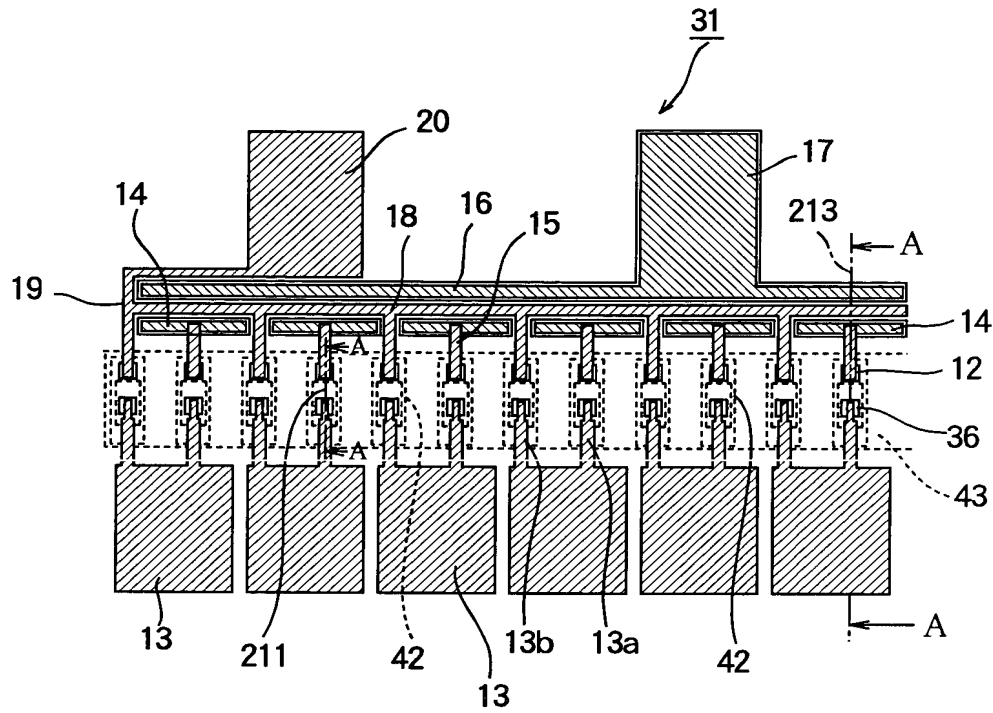


FIG.13B

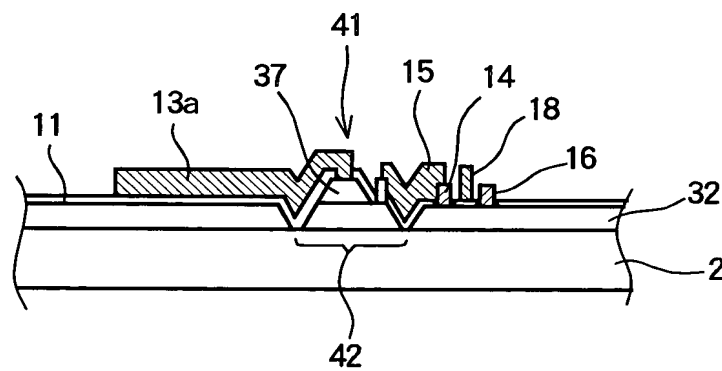
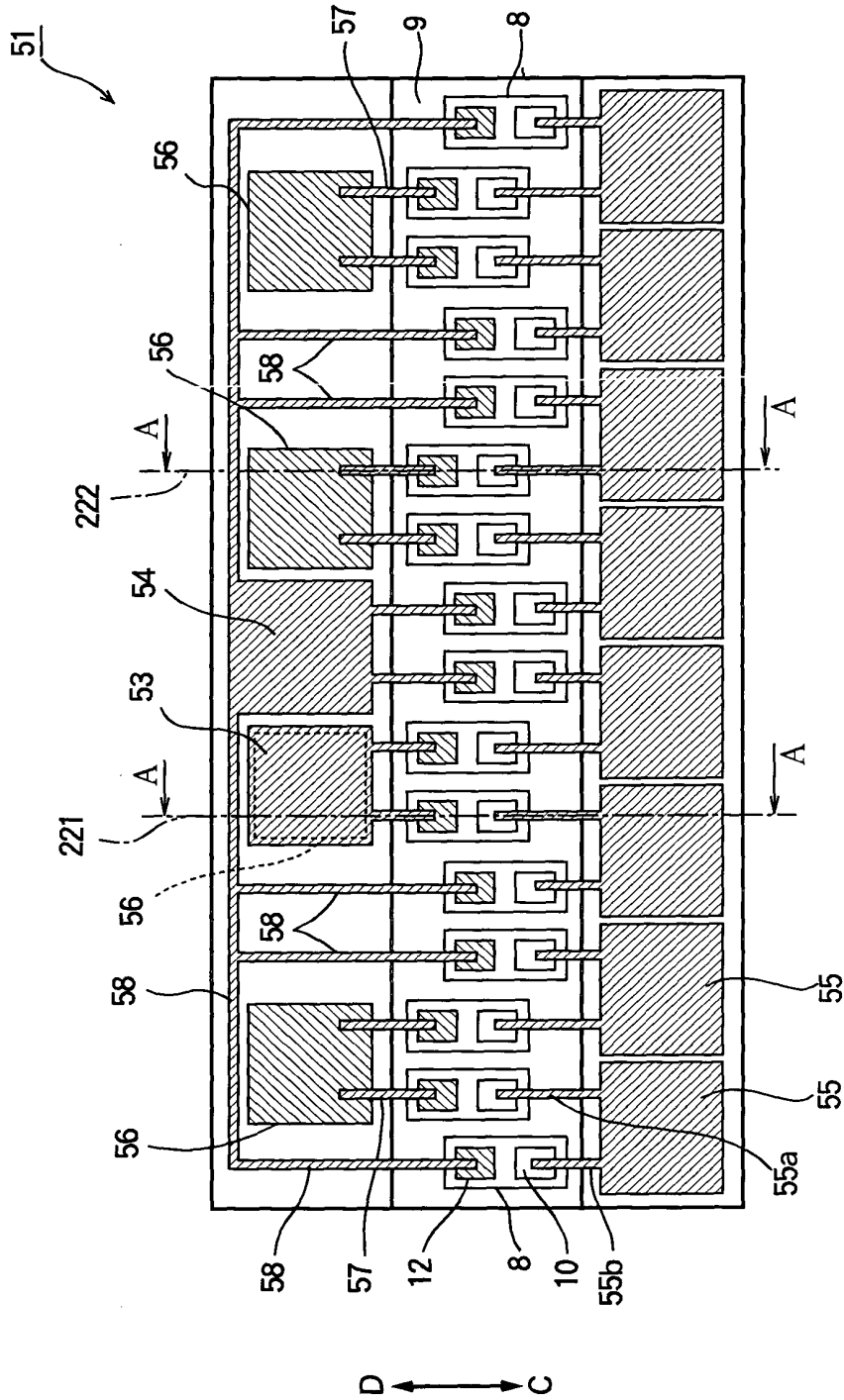


FIG. 14



[illegible]

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FIG. 16A

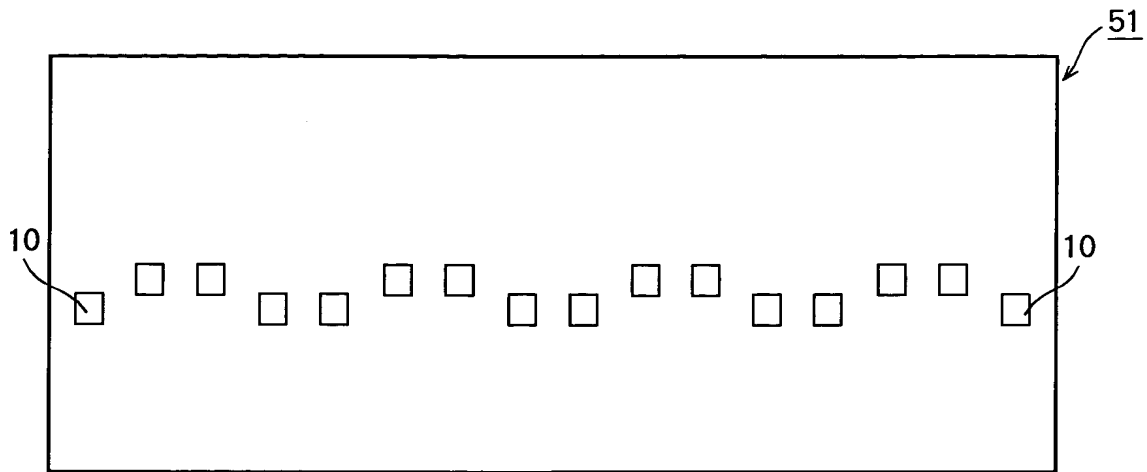


FIG. 16B

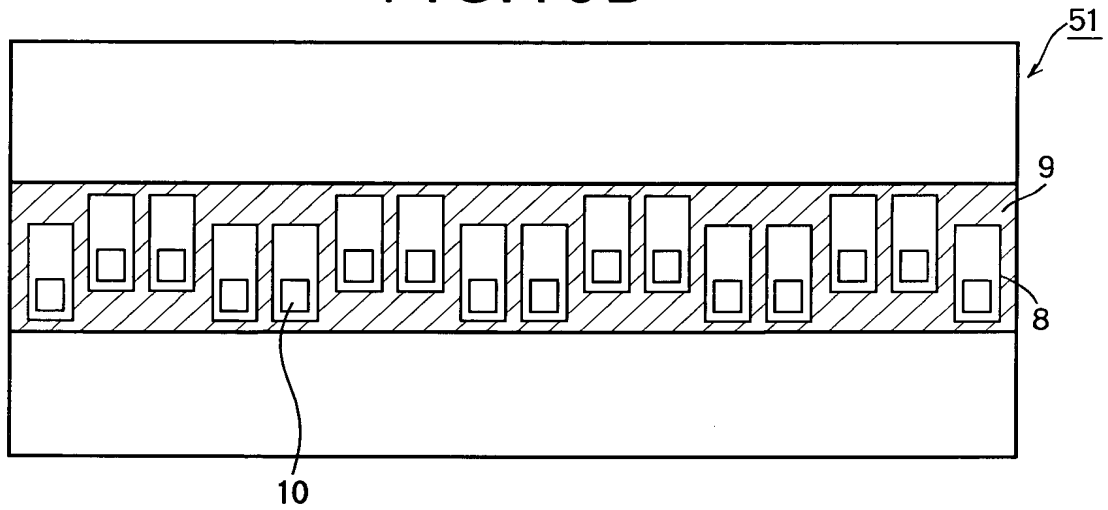
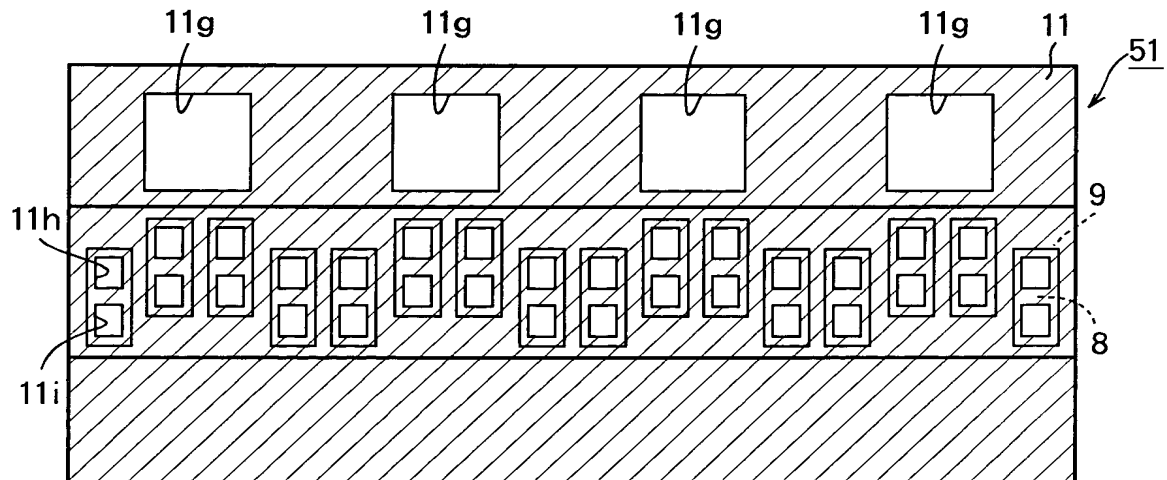


FIG. 16C



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FIG. 17A

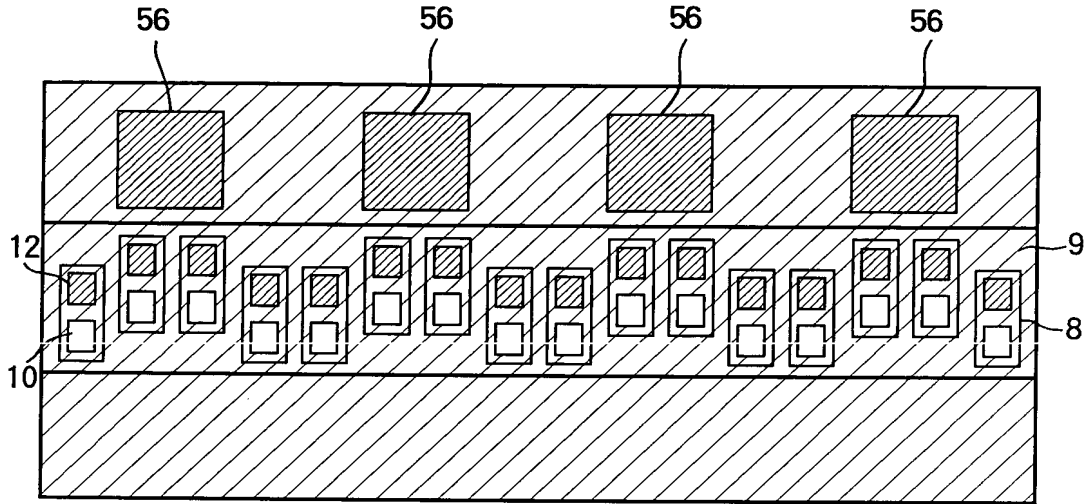
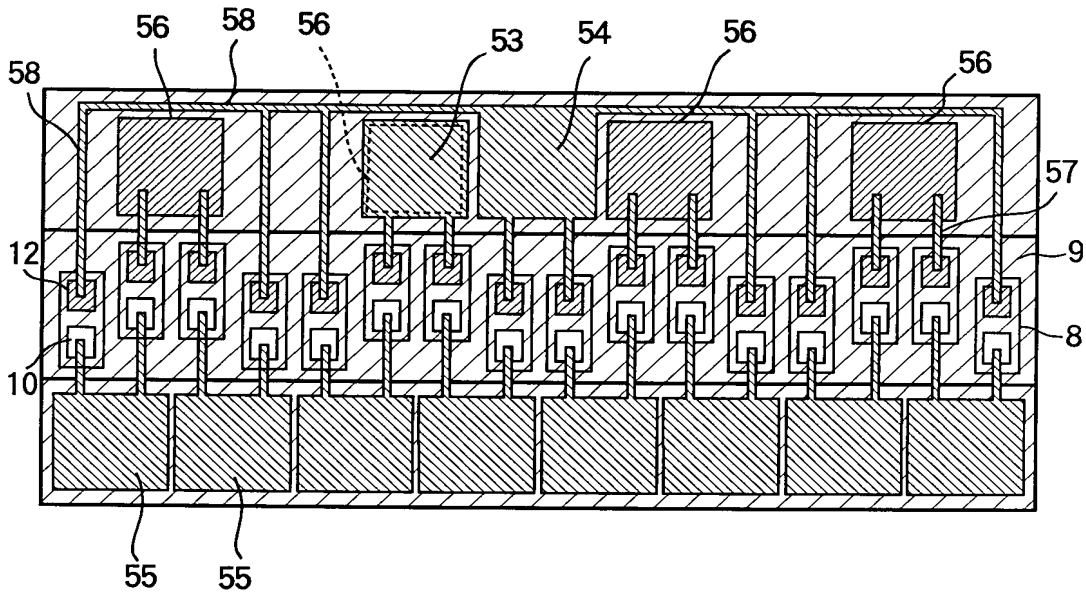


FIG. 17B



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FIG. 19

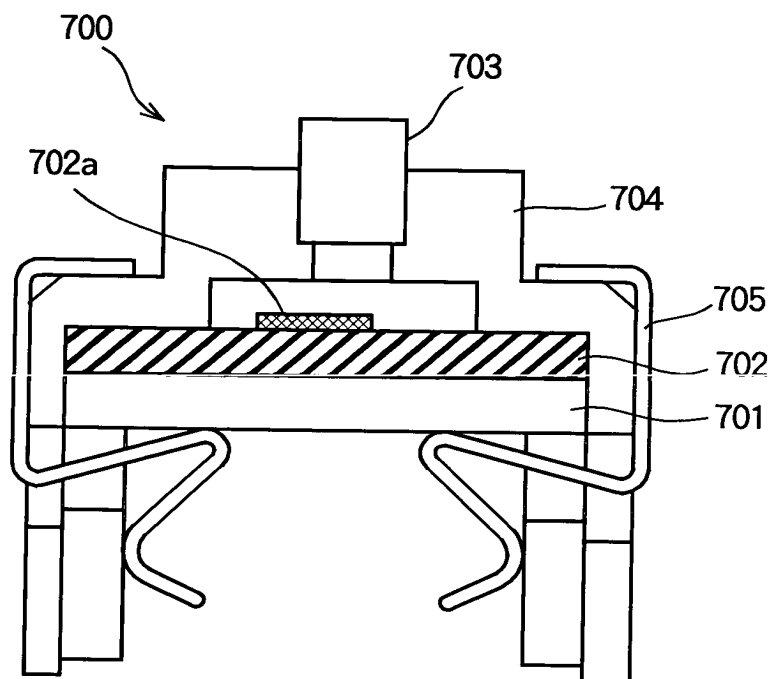


FIG. 20

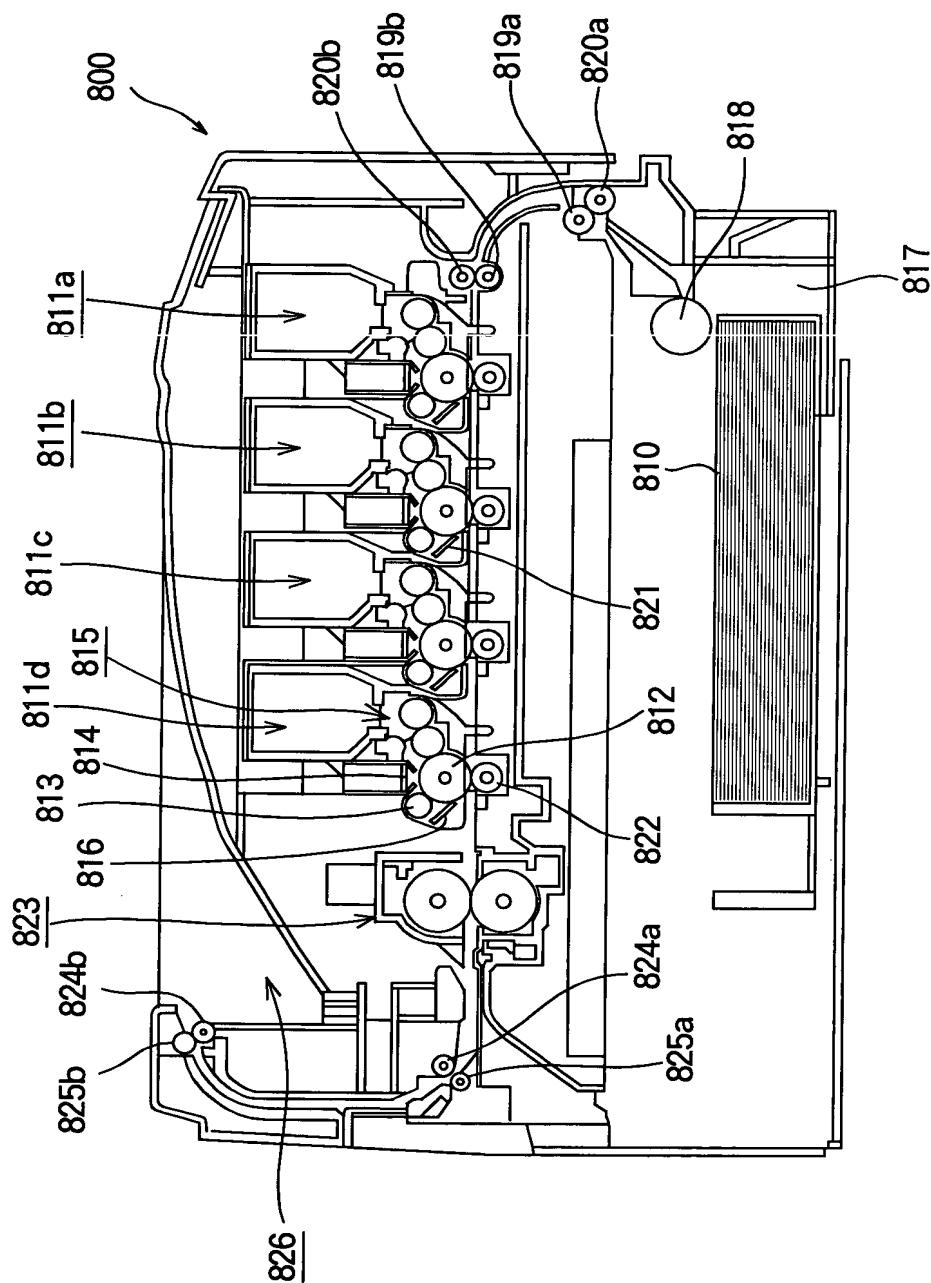


FIG. 21
 PRIOR ART

